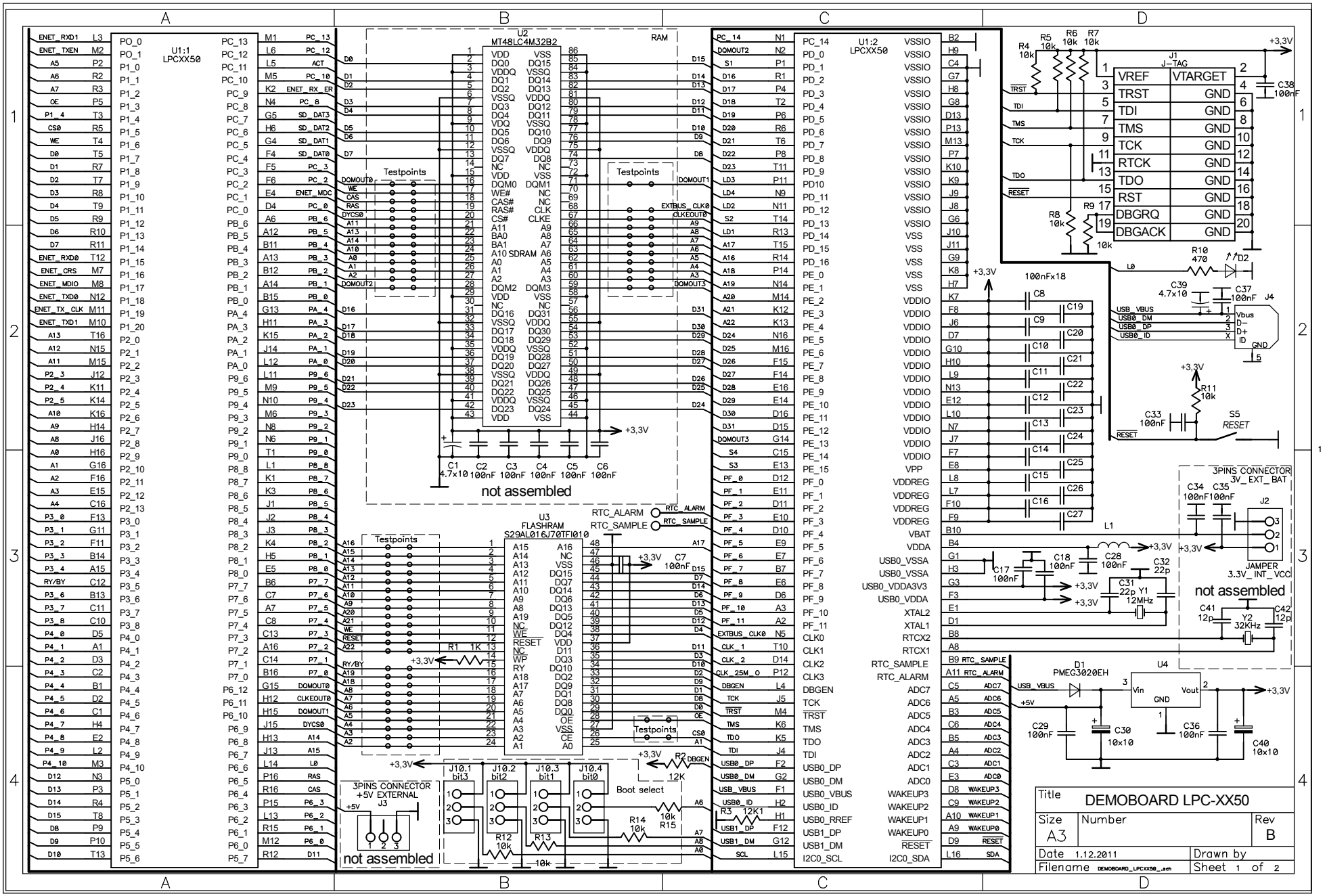
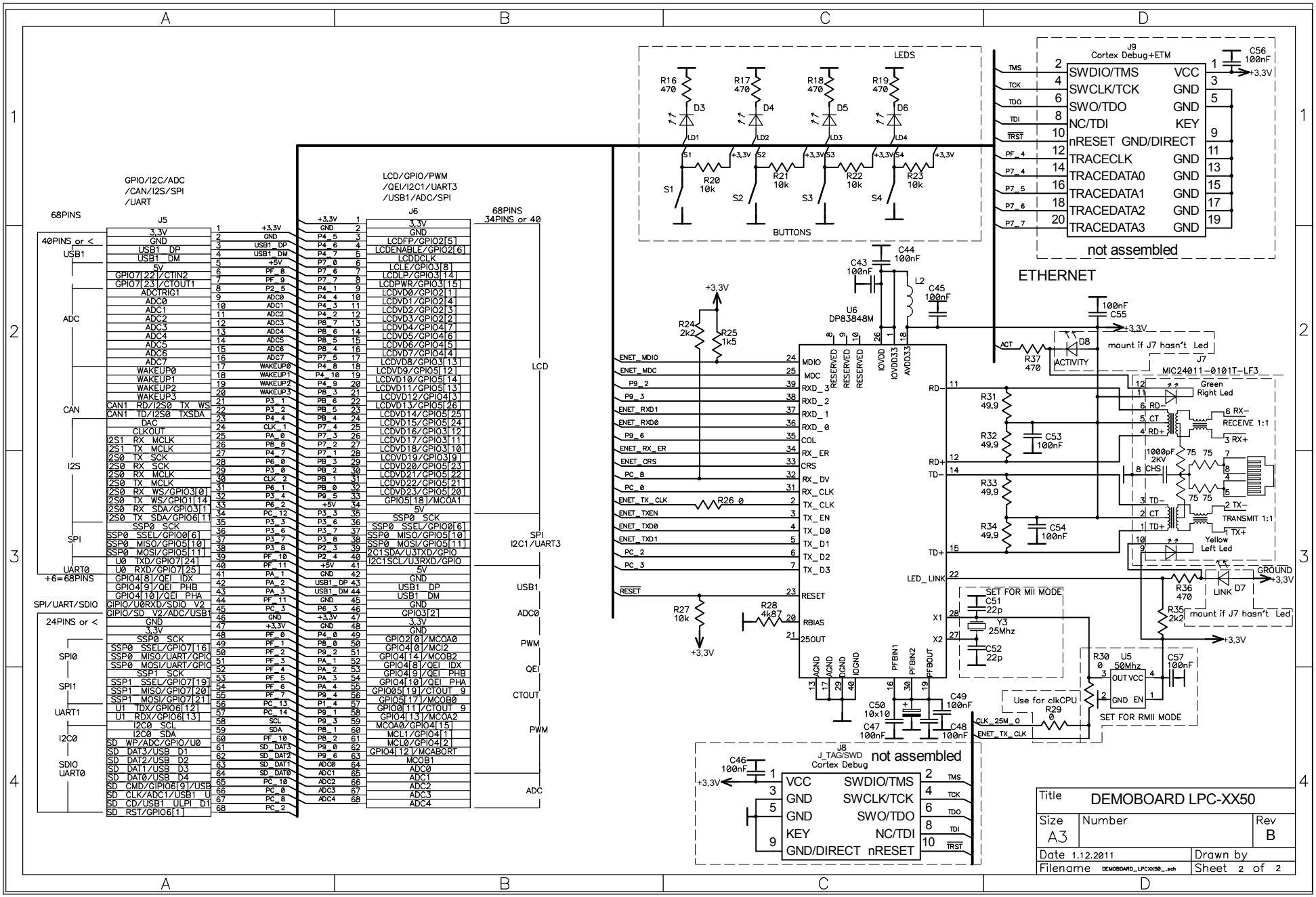

U1:1
LPCXX50
U1:2
LPCXX50
U2
MT48LC4M32B2
RAM
U3
FLASHRAM
S29AL016J70TFI010
Testpoints
not assembled
3PINS CONNECTOR
+5V EXTERNAL
J3
J10.1 bit3
J10.2 bit2
J10.3 bit1
J10.4 bit0
Boot select
RTC_ALARM
RTC_SAMPLE
J1
J-TAG
VREF
VTARGET
TRST
TDI
TMS
TCK
RTCK
TDO
RST
DBGRQ
DBGACK
GND
J4
USB_VBUS
USB0_DM
USB0_DP
USB0_ID
Vbus
D-
D+
ID
S5
RESET
3PINS CONNECTOR
3V_EXT_BAT
J2
JAMPER
3.3V_INT_VCC
Y1
12MHz
Y2
32KHz
D1
PMEG3020EH
U4
Vin
Vout
GND
Title
DEMOBOARD LPC-XX50
Size
A3
Number
Rev
B
Date
1.12.2011
Drawn by
Filename
DEMOBOARD_LPCXX50_.sch
Sheet 1 of 2


GPIO/I2C/ADC
/CAN/I2S/SPI
/UART
68PINS
J5
40PINS or <
USB1
ADC
CAN
I2S
SPI
UART0
+6=68PINS
SPI/UART/SDIO
24PINS or <
SPI0
SPI1
UART1
I2C0
SDIO
UART0
LCD/GPIO/PWM
/QEI/I2C1/UART3
/USB1/ADC/SPI
J6
68PINS
34PINS or 40
LCD
SPI
I2C1/UART3
USB1
ADC0
PWM
QEI
CTOUT
PWM
ADC
LEDS
BUTTONS
J9
Cortex Debug+ETM
not assembled
ETHERNET
U6
DP83848M
J7
MIC24011-0101T-LF3
Green
Right Led
RECEIVE 1:1
TRANSMIT 1:1
Yellow
Left Led
SET FOR MII MODE
SET FOR RMII MODE
Use for clkCPU
mount if J7 hasn't Led
J8
J_TAG/SWD
Cortex Debug
not assembled
Title
DEMOBOARD LPC-XX50
Size
A3
Number
Rev
B
Date 1.12.2011
Drawn by
Filename DEMOBOARD_LPCXX50_.sch
Sheet 2 of 2