

LPC1850 - Connectors and Jumpers

J5 Connector (GPIO/I2C/ADC/CAN/I2S/SPI/UART)

Pins	GPIO pin	Additional functionality	Ports
1	3.3V		
2	GND		
3	USB1_DP	USB1_DP	USB1
4	USB1_DM	USB1_DM	
5	5V		
6	PF_8	GPIO0[22], CTIN2	GPIO
7	PF_9	GPIO0[24], CTOUT1	
8	P2_5	ADCTRIG1	ADC
9	ADC0	ADC0	
10	ADC1	ADC1	
11	ADC2	ADC2	
12	ADC3	ADC3	
13	ADC4	ADC4	
14	ADC5	ADC5	
15	ADC6	ADC6	
16	ADC7	ADC7	
17	WAKEUP0	WAKEUP0	CAN
18	WAKEUP1	WAKEUP1	
19	WAKEUP2	WAKEUP2	
20	WAKEUP3	WAKEUP3	
21	P3_1	CAN1_RD/I2S0_TX_WS	
22	P3_2	CAN1_TD/I2S0_TXSDA	

23	P4_4	DAC	I2S
24	CLK_1	CLKOUT	
25	PA_0	I2S1_RX_MCLK	
26	P8_8	I2S1_TX_MCLK	
27	P4_7	I2S0_TX_SCK	
28	P6_0	I2S0_RX_SCK	
29	P3_0	I2S0_RX_MCLK	
30	CLK_2	I2S1_TX_MCLK	
31	P6_1	I2S0_RX_WS, GPIO3[0]	
32	P3_4	I2S0_TX_WS, GPIO1[14]	
33	P6_2	I2S0_RX_SDA, GPIO3[1]	SPI
34	PC_12	I2S0_TX_SDA, GPIO6[11]	
35	P3_3	SSP0_SCK	
36	P3_6	SSP0_SSEL, GPIO0[6]	UART0
37	P3_7	SSP0_MISO, GPIO5[1]	
38	P3_8	SSP0_MOSI, GPIO5[2]	
39	PF_10	U0_TXD, GPIO0[25]	UART0
40	PF_11	U0_RXD, GPIO0[26]	
41	PA_1	GPIO4[8], QEI_IDX	
42	PA_2	GPIO4[9], QEI_PHB	
43	PA_3	GPIO4[10], QET_PHA	
44	PF_11	GPIO, U0RDX, SDIO_V2	
45	ENET_TX03	GPIO, SD_V2, ADC, USB1	
46	GND		
47	3.3V		

48	PF_0	SSP0_SCK	SPI0
49	PF_1	SSP0_SSEL, GPIO0[17]	
50	PF_2	SSP0_MISO, UART, GPIO7[17]	
51	PF_3	SSP0_MOSI, UART, GPIO7[18]	
52	PF_4	SSP1_SCK	SPI1
53	PF_5	SSP1_SSEL, GPIO0[20]	
54	PF_6	SSP1_MISO, GPIO0[21]	
55	PF_7	SSP1_MOSI, GPIO0[22]	
56	PC_13	U1_TDX, GPIO6[12]	UART1
57	PC_14	U1_RDX, GPIO6[7]	
58	SCL	I2C0_SCL	I2C0
59	SDA	I2C0_SDA	
60	PF_10	SD_WP, ADC, GPIO, U0_TXD	SDIO, UART0
61	SD_DAT3	SD_DAT3, USB_D1	
62	SD_DAT2	SD_DAT2, USB_D2	
63	SD_DAT1	SD_DAT1, USB_D3	
64	SD_DAT0	SD_DAT0, USB_D4	
65	PC_10	SD_CMD, GPIO6[9], USB	
66	ENET_RX_CLK	SD_CLK, ADC1, USB1	
67	ENET_RX_DV	SD_CD, USB1, ULPI	
68	ENET_TXD2	SD_RST, GPIO6[1]	

J6 Connector(LCD/GPIO/PWM/QEI/I2C1/UART3/USB1/ADC/SPI)

Pins	GPIO pin	Additional functionality	Ports
1	+3.3V	3.3V	

2	GND	GND	
3	P4_5	LCDFP, GPIO2[5]	LCD
4	P4_6	LCDENABLE, GPIO2[6]	
5	P4_7	LCDDCLK	
6	P7_0	LCLE, GPIO3[8]	
7	P7_6	LCDLP, GPIO3[14]	
8	P7_7	LCDPWR, GPIO3[15]	
9	P4_1	LCDVD0, GPIO2[1]	
10	P4_4	LCDVD1, GPIO2[4]	
11	P4_3	LCDVD2, GPIO2[3]	
12	P4_2	LCDVD3, GPIO2[2]	
13	P8_7	LCDVD4, GPIO4[7]	
14	P8_6	LCDVD5, GPIO4[6]	
15	P8_5	LCDVD6, GPIO4[5]	
16	P8_4	LCDVD7, GPIO4[3]	
17	P7_5	LCDVD8, GPIO3[13]	
18	P4_8	LCDVD9, GPIO5[3]	
19	P4_10	LCDVD10, GPIO5[5]	
20	P4_9	LCDVD11, GPIO5[4]	
21	P8_3	LCDVD12, GPIO4[3]	
22	P8_6	LCDVD13, GPIO5[26]	
23	P8_5	LCDVD14, GPIO5[25]	
24	P8_4	LCDVD15, GPIO5[24]	
25	P7_4	LCDVD16, GPIO3[12]	
26	P7_3	LCDVD17, GPIO3[11]	

27	P7_2	LCDVD18, GPIO3[10]	
28	P7_1	LCDVD19, GPIO3[9]	
29	P8_3	LCDVD20, GPIO5[22]	
30	P8_2	LCDVD21, GPIO5[12]	
31	P8_1	LCDVD22, GPIO5[21]	
32	P8_0	LCDVD23, GPIO5[20]	
33	P9_5	GPIO5[18], MCOA1	
34	+5V	5V	
35	P3_3	SSP0_SCK	SPI, I2C1, UART3
36	P3_6	SSP0_SSEL, GPIO0[6]	
37	P3_7	SSP0_MISO, GPIO5[1]	
38	P3_8	SSP0_MOSI, GPIO5[2]	
39	P2_3	I2C1SDA, U3TXD, GPIO12	
40	P2_4	I2C1SCL, U3RXD, GPIO13	
41	+5V	5V	
42	GND	GND	
43	USB1_DP	USB1_DP	USB1
44	USB1_DM	USB1_DM	
45	GND	GND	
46	P6_3	GPIO3[2]	PWM, QEI, CTOUT
47	+3.3V	3.3V	
48	GND	GND	
49	P4_0	GPIO2[0], MCOA0	
50	P8_0	GPIO4[0], MCI2	
51	ENET_RX03	GPIO4[14], MCOB2	

52	PA_1	GPIO4[8], QEI_IDX	
53	PA_2	GPIO4[9], QEI_PHB	
54	PA_3	GPIO4[10], QEI_PHA	
55	PA_4	GPIO5[19], CTOUT_9	
56	P9_4	GPIO5[17], MCOB0	
57	P1_4	GPIO0[11], CTOUT_9	
58	P9_1	GPIO4[13], MCOA2	
59	ENETRxD2	MCOA0, GPIO4[15]	
60	P8_1	MCL1, GPIO4[1]	
61	P8_2	MCL0, GPIO4[2]	
62	P9_0	GPIO4[12], MCABORT	
63	ENET_COL	MCOB1	
64	ADC0	ADC0	
65	ADC1	ADC1	
66	ADC2	ADC2	
67	ADC3	ADC3	
68	ADC4	ADC4	